

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

En-Hsing Chen, et al.

Title:

NAND MEMORY ARRAY INCORPORATING MULTIPLE SERIES

SELECTION DEVICES AND METHOD FOR OPERATION OF SAME

Application No.: 10/729,865

Filed:

December 5, 2003

Examiner:

Nguyen, Van Thu T.

Group Art Unit:

2824

Atty. Docket No.: 023-0029

Confirmation No.:

8494

December 7, 2005

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INFORMATION DISCLOSURE STATEMENT 37 C.F.R. § 1.97(b)

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Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the undersigned brings to the Examiner's attention in the above-identified application the patents, publications, applications or other information identified in the attached:

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\boxtimes	Form(s) PTO/SB/08B (1 page(s), with copy of 3 reference(s)).
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Citation of such information shall not be construed as (i) an admission that the information necessarily is, or corresponds to, prior art with respect to the instant invention, (ii) a representation that a search has been made, other than as described herein, or (iii) an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

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	Certain information was submitted to, or cited by, the Office in Application No(s). N/A, filed N/A, to which the above-identified application claims priority under 35 U.S.C. § 120. In accordance with 37 C.F.R. § 1.98(d), copies of that information are not included.						
	Pursuant to 1287 OG 163 (October 19, 2004), for those cited pending U.S. patent applications filed (or which entered the national stage under 35 U.S.C. § 371) on or after June 30, 2003 or which are otherwise stored in the USPTO's Image File Wrapper system, copies are not included.						
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	This Information Disclosure Statement is filed within three months of the filing date of a national application other than a continued prosecution application under § 1.53(d) or within three months of entry of the national stage as set forth in § 1.491 in an international application. Therefore, no fee is required.						
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I hereby correspor	certify that, on the date shown below, this ndence is being sited with the US Postal Service with sufficient postage st class mail and addressed as shown above. mile transmitted to the US Patent and Trademark Office.	Andrew C. Graham, Reg. No. 36,531 Attorney for Applicant(s) (512) 338-6313 (direct)					

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PF SB 08A (Substitute for form 1449/PTO)	Attorney Docket No.:	023-0029
- of \	Application No.:	10/729,865
12 1005 ISTORMATION DISCLOSURE STATEMENT BY APPLICANT	First Named Inventor:	En-Hsing Chen, et al.
& TRACE	Filing Date:	December 5, 2003
& TRANSFER	Group Art Unit:	2824
	Examiner Name:	Nguyen, Van Thu T.
Sheet 1 of 2	Date Submitted:	December 7, 2005

			U.S. PATENT DO	CUMENTS		
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
	AA	5,299,166	03-29-1994	Suh et al.		_
	AB	6,115,287	09-05-2000	Shimizu et al.		
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	AN	WO-2002-43067 US	05-2002			
Examiner Signature				Date Considered		

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Applicant's unique citation designation number (optional).

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PTO/SB/08A (Substitute for form 1449/PTO)	Attorney Docket No.:	023-0029	
	Application No.:	10/729,865	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	First Named Inventor: En-Hsing Chen, et al.		
(Continuation Sheet)	Filing Date:	December 5, 2003	
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	Examiner Name:	Nguyen, Van Thu T.	
Sheet 2 of 2	Date Submitted:	December 7, 2005	

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			U.S. PATENT DO	CUMENTS		
Examiner Initials*	Cite No.1	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
	AN	US 2005/0121790	06-09-2005	Cleeves et al.		
	AO	US 2003/0109093	06-12-2003	Harari et al.	·	
	AP	US 2005/0018485 A1	01-27-2005	Noguchi et al.		•
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		<u> </u>	FOREIGN PATENT	DOCUMENTS		Τ.
Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ -Number ⁴ - Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T
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10/SB/08B	(Substitu	te for form 1449/PTO)	Attorney Docket No.:	023-0029	
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			Filing Date:	December 5,	2003
Group Art Unit: 2824					
			Examiner Name:	Nguyen, Van	Thu T.
Sheet 1 of 1			Date Submitted:	December 7,	2005
		NON PATENT LITERATURE DOCUM	ENTS		
Examiner Cite Include name of author (in CAPITAL LETTERS), title of article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.					
	AA	KIM ET AL. "Scaled SONOSFET NAND Flash EEPROM; The NA Scaled SONOSFET", Transcripts KIEE, Vol. 49C, No. 1, January 2		I using the	Abstract only
	AB	KOBAYASHI ET AL., "TA 7.2: A 3.3V-Only 16 Mb DINOR Flas Memory, 1995 IEEE International Solid-State Circuits Conference,		sion 7, Flash	,
	AC	NAJI, PETER K. ET AL., "A 256kb 3.0V 1T1MTJ Nonvolatile Ma ISSCC, February 6, 2001, Paper 7.6, and corresponding presentation		1 IEEE	
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